

An Approach To Develop A Modified Resonant Single Ended Primary Inductor Converter With High Efficiency And Wide Voltage Ranges

¹Ninsha Miriyam Raju, ²J Velmurugan

¹ME Student, Department of Electrical & Electronics Engineering, PSNACET, Dindigul, Tamilnadu, India.

²Asso. Professor, Department of Electrical & Electronics Engineering, PSNACET, Dindigul, Tamilnadu, India.

E-mail: ¹edensnina@gmail.com, ²vel_76@yahoo.co.in

ABSTRACT

This work reports a modified resonant SEPIC converter. It provides better efficiency over a wide input and output voltage range, simple circuit and small size. Its control method gives a fast transient response and efficient light load operation. The converter achieves higher than 87% efficiency across the entire input voltage range at nominal output voltage and maintains good efficiency across the whole operating range. Here we set up a modified SEPIC converter with switching frequency 20 MHz. Its input voltage ranges from 3.6 V to 7.2 V and its output voltage ranges from 3 V to 9 V. Its rated power is 3 W. Here we are using a PID controller which doesn't have sustained oscillations. This design is suitable for adaptive bias control of an RF power amplifier from a battery input, digital cameras, cellular phones, laptops, servers etc...

Keywords: Single ended primary inductor converter (SEPIC) converter, soft switching, PID controller, and ON-OFF control.

1. INTRODUCTION

Power electronic technology has gone through dynamic evolution in the last four decades. Recently, its applications are fast expanding in industrial, commercial, residential, transportation, utility, aerospace, and military environments primarily due to reduction of cost, size, and improvement of performance. In the global industrial automation, energy conservation, and environmental pollution control trends of the 21st century, the widespread impact of power electronics is inevitable. It appears that the role of power electronics in our society in the future will tend to be as important and versatile as that of information technology today.

Power electronic converters are a family of electrical circuits which convert electrical energy from one level of voltage/current/frequency to the other using semiconductor-based electronics switches. The input and output may be alternating current or direct current. As the power electronics industry has developed, various families of power electronic converters have evolved, often linked by power level, switching devices, and topological origins. The process of switching the electronic devices in a power electronic converter from one state to another is called modulation, and the development of optimum strategies to implement this process has been the subject of intensive international research efforts for at least 30 years. Each family of power converters has preferred modulation strategies associated with it that aim to optimize the circuit operation for the target criteria most appropriate for that family. Parameters such as switching frequency, distortion, losses, harmonic generation, and speed of response are typical

of the issues which must be considered when developing modulation strategies for a particular family of converters. The end goals of a power electronic converter are to achieve high efficiency of conversion, minimize size and weight, and achieve desired regulation of the output.

The DC-DC converter is an electrical circuit that transfers energy from a DC voltage source to a load. In a dc-dc converter, both the input and the output are dc, and in the simplest case the output voltage need to be regulated in presence of variation in load current and changes in the input voltage. The switches are transistors and diodes; the storage devices are inductors and capacitors. This process of energy transfer results in an output voltage that is related to the input voltage by the duty ratios of the switches. In addition to the constraints of size, weight, and cost, DC-DC converter technology also addresses the issues of efficiency and regulation. DC/DC converters are used in most mobile devices to maintain the voltage at a fixed value whatever the voltage level of the battery is. These converters are also used for electronic isolation and power factor correction.

SEPIC is a type of DC-DC converter allowing the electrical voltage at its output to be greater than, less than, or equal to that at its input; the output of the SEPIC is controlled by the duty cycle of the control transistor. A SEPIC is similar to a traditional buck-boost converter; but has advantages of having non-inverted output. As with other switched mode power supplies the SEPIC exchanges energy between the capacitors and inductors in order to convert from one voltage to another. The amount of energy exchanged is controlled by switch S_1 , which is typically a

transistors such as a MOSFET; MOSFET suffers much higher input impedance and lower voltage droop than bipolar junction, and does not require biasing resistors.

2. RELATED WORK

Sweta Srivastava, Sanjay Kumar Singh propose a power convertible power electronic application we would be able to achieve high efficiency with variable input and output ranging at a very small size. In an effort to reduce the component count and also improve the performance of single stage power factor correction techniques have been implemented. The concept of variable input and output voltages with reducing the component size to small without hampering the efficiency, are also presented [2].

James R. Warren, III, Kathryn Anne Rosowski, and David J. Perreault proposed in this document which explores the design of dc-dc converters operating in the VHF frequency range (30–300 MHz). It presents a method for evaluating transistors and selecting operating frequencies for class-E-based power converter employing sinusoidal resonant gating [4].

R. C. Pilawa-Podgurski, A. D. Sagneri, J. M. Rivas, D. I. Anderson, and D. J. Perreault proposed a resonant boost converter topology and control methods suitable for designs at very high frequency. This provides low transistor voltage stress, and requires small passive components, allowing for very fast transient response [5].

J. Rivas, R. Wahby, J. Shafran, and D. Perreault proposed a new architecture for dc-dc power conversion that enables dramatic increases in switching frequencies, potentially into the microwave/ultra-high frequency (UHF) range. The architecture of most conventional systems is straightforward: a single power stage of a particular topology regulated using a switching control technique such as pulse-width modulation (PWM) or frequency modulation. The passive components provide intermediate energy storage in the conversion process and provide filtering to attenuate the switching ripple to acceptable levels [6].

3. ABOUT THE NEW TECHNOLOGY

This paper introduces a modified high-frequency resonant SEPIC converter and its control methods. In previous converter designs they face many problems like hard switching, less efficiency, complex circuitry and slow transient response. But in this proposed approach it provides high efficiency over wide input and output voltage ranges. In this it requires less energy storage, and thereby we can achieve fast transient response. In case of conventional resonant converters it needs bulk inductors but here we can eliminate the use of bulk inductors and we can achieve portable electronic converters with small size. The proposed design operates over wide input range of 3.6V to 7.2V, output range of 3V to 9V and power range of 0.3W to 3W. This design is suitable for adaptive bias control of an RF power amplifier from a battery input, digital cameras, cellular phones, laptops, servers etc...

Fig. 1. shows the circuit diagram of the proposed SEPIC converter topology. There are many similarities between the proposed converter and other conventional converters. The main difference is in its control strategy, component placement and sizing.

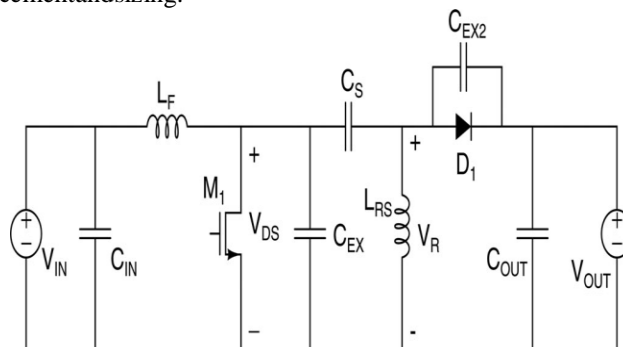


Fig. 1. Proposed resonant SEPIC converter topology

In case of a conventional SEPIC converter there are two bulk inductors present. This will lead to hard switching of the switch and diode. In case of quasi-resonant SEPIC converters L_F is replaced with resonant inductors. In case of multi-resonant SEPIC converters it also uses bulk inductors, but in order to achieve soft switching capacitors are connected in parallel with the switch and diode. In this proposed modified resonant SEPIC converter there is no bulk inductors present. It uses two resonant inductors: one inductor L_F resonates with the network capacitance, $C_{OSS} + C_{EX}$, for resonant inversion, while the other inductor L_{RS} resonates with the rectifier capacitance C_{EX2} for resonant rectification. This will help to improve the response speed and reduce the component number.

The other major difference between conventional and modified SEPIC converter is in its control strategy. In case of conventional SEPIC converter it regulates the output voltage by making the ON time fixed and varies the OFF time. This will lead to variable frequency variable duty ratio operation. But here in this modified resonant SEPIC converter it uses a fixed frequency fixed duty ratio. This helps in elimination of bulk components, device stress and enables soft switching over wide input and output voltage ranges. In this topology we are using PID controller technique.

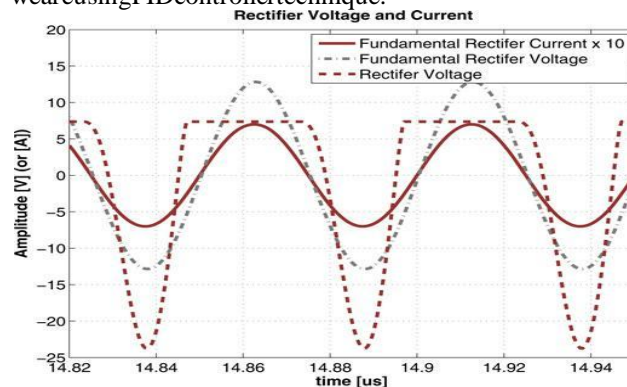


Fig. 2. Resonant rectifier voltage and current

The operation of this modified resonant SEPIC converter can be understood by connecting two subsystems. That is one resonant rectifier and one resonant inverter. Here in this we are redesigning the rectifier and the inverter individually, and

together then retuning as necessary to account for non-linear interactions between the inverter and rectifier. In the above fig. 2 it shows the fundamental of rectifier voltage V_r and current I_r of the resonant rectifier of tuned to look resistive at an operating frequency of 20 MHz. Here the fundamental component of the input voltage and the current are in phase. As the value of L and C_{EX2} are changed, output power level and the phase relationship between V_r and I_r change. As the phase difference between V_r and I_r increases, the losses due to reactive currents rise, reducing the output power and the overall efficiency of the rectifier.

4. CONTROL STRATEGY

Here we use an ON-OFF control scheme; in which switching of the SEPIC converter is gated ON and OFF to control the average power delivered to the output. The frequency at which the converter is modulated ON and OFF is much lower than the converter switching frequency. In this proposed scheme, the components are sized for high switching frequency while the power converter input and output filters are sized for lower modulation frequency.

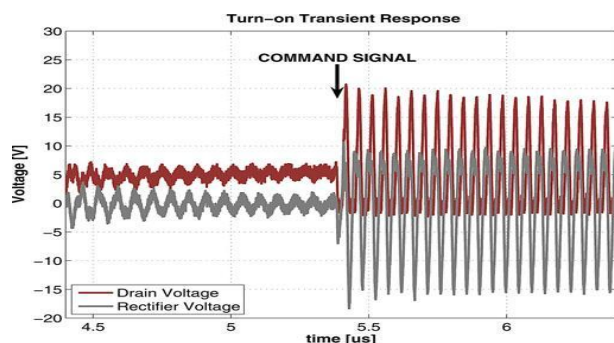


Fig.3. Turn-ON transient response

In previous converter designs they used the ON-OFF control with voltage hysteresis control method. This ON-OFF voltage hysteresis method has certain advantages like high efficiency at light load, controlled voltage band and good stability. But the input and output voltage waveforms have variable frequency. Therefore this control became undesirable in many applications and it increases the difficulty of filter design.

To face the different problems in control we use a new approach, in which the on time of the converter is PWM with a fixed modulation period, is utilized to implement the ON-OFF control method. In case of hysteresis ON-OFF control it has a variable frequency operation. But in PWM ON-OFF control it operates at a fixed modulation frequency. It leads to well-defined frequency content at the converter input and output. On the other side, efficiency tends to reduce at extremely light loads, when the converter may operate for only a few switching cycles each modulation period. Characteristics of this control method are similar in many regards to conventional fixed-frequency PWM. However, instead of modulating the voltage applied to a filter, the current delivered to the output capacitor and load is modulated in this technique.

The controller in this proposed modified SEPIC is implemented with a conventional PWM chip. In which the PWM output is the enable signal of the power stage gate drive. The converter power stage can be modeled as a one-pole system with the converter approximated as a controlled current source feeding the output capacitor and load C_{OUT} and R_{LOAD} , where R_{LOAD} is the effective load resistance of the converter.

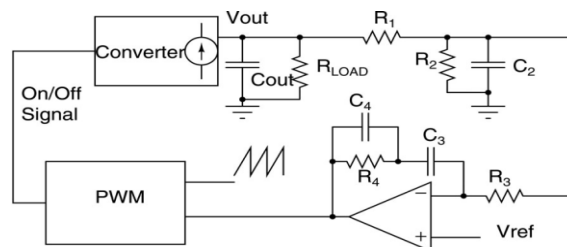


Fig.4. ON/OFF PWM control of a resonant dc-dc converter

In this proposed topology we are using PID controller. Using a PID controller can give you performance that is better than using proportional or integral alone, or even a PI or PD. The controller attempts to correct the error between a measured process variable and desired setpoint by calculating the difference and then performing a corrective action to adjust the process accordingly. A PID controller controls a process through three parameters: Proportional (P), Integral (I), and Derivative (D). These parameters can be weighted, or tuned, to adjust their effect on the process. PID controllers are a type of continuous controller because they continually adjust the output vs. an on/off controller, when looking at feed forward or feedback conditions.

5. SIMULATION AND HARDWARE RESULTS

This section gives the simulation results of the proposed modified resonant single-ended primary inductor converter (SEPIC) converter. The converter operates at 20 MHz.

TABLE I
EXPERIMENTAL SPECIFICATIONS

Input Voltage Range	3.6 V - 7.2 V
Output Voltage Range	3 V - 9 V
Switching Frequency	20 MHz
Output Power	0.3 W - 3 W

This simulation circuit and results are given below.

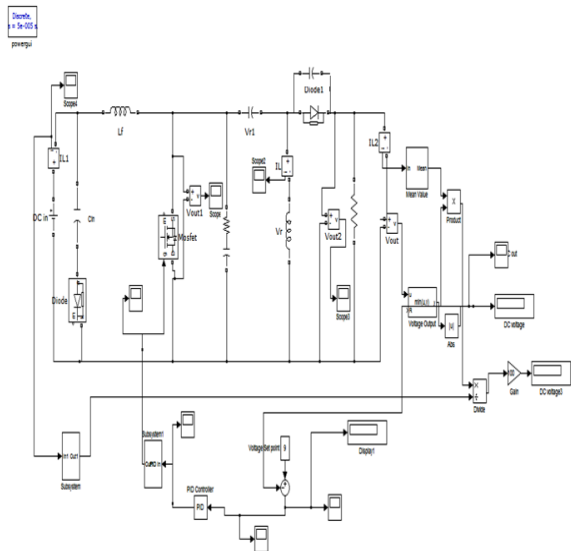


Fig.5.Power circuitofthesystem

Thefig.5showsthepowercircuitforthe modifiedsepic converter.Here weareusingMATLAB software torepresent thesimulation.

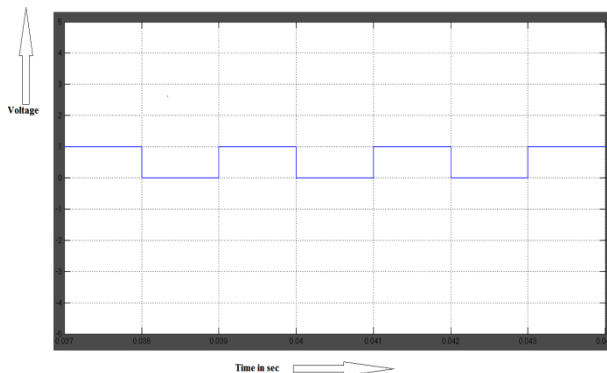


Fig.6.Triggeringpulses

Fig.6.showstheoutputsof thesimulation.Thesearethe triggeringpulseswhicharegiven totheMOSFET.Here we aregeneratingPWMsignalstotriggertheMOSFET.

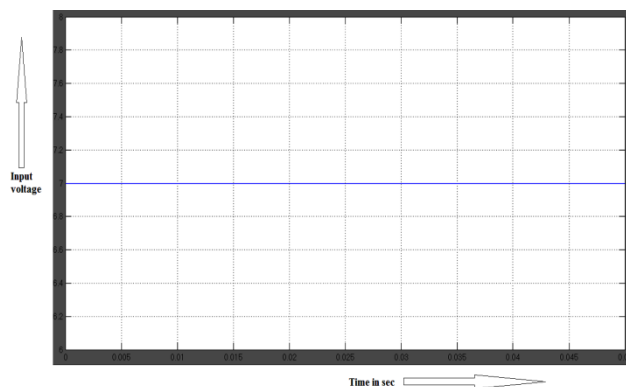


Fig.7.Inputvoltagewaveform

Fig.7. shows theinput voltage waveform. Here we are givinganinputvoltageof7V.

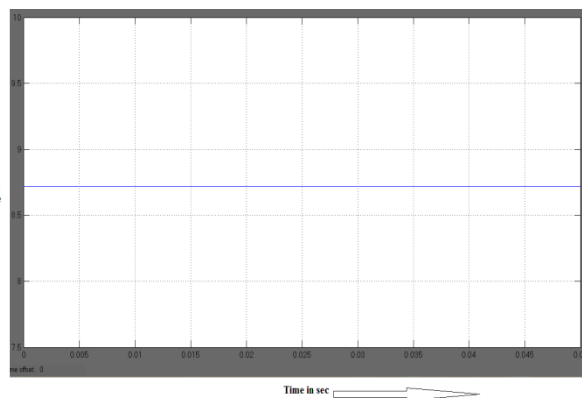


Fig.8.Outputvoltagewaveform

Fig.8.showstheoutputvoltage waveform.Here we get an outputof8.7V.

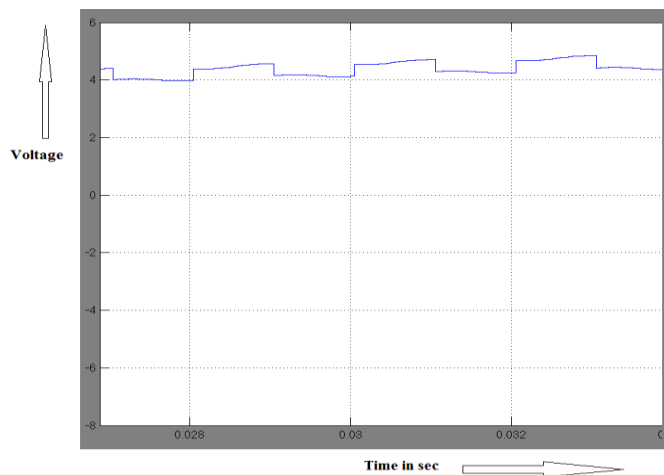


Fig.9.VoltageacrossMOSFET

Fig.9.shows thevoltageacrosstheMOSFET.

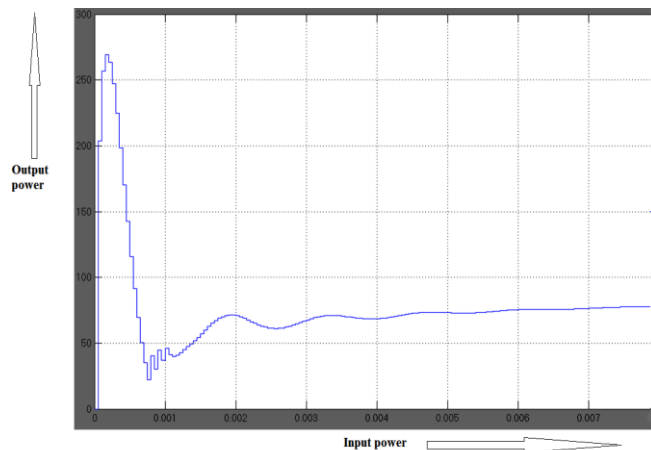


Fig.10.Efficiency waveformofPI controller

Fig. 10.showstheefficiencywaveformofPIcontroller.It has moreoscillationscomparedtoPIDcontroller.Also the efficiencyisless.

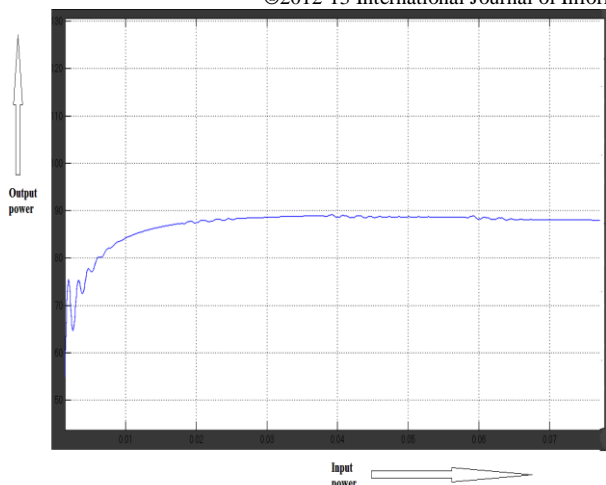


Fig.11. Efficiency waveform of PID controller

Fig.11. shows the efficiency waveform of PID controller. It has fewer oscillations compared to PI controller. Also the efficiency is more than that of PI. Its efficiency is greater than 87%.

The hardware block diagram is given below.

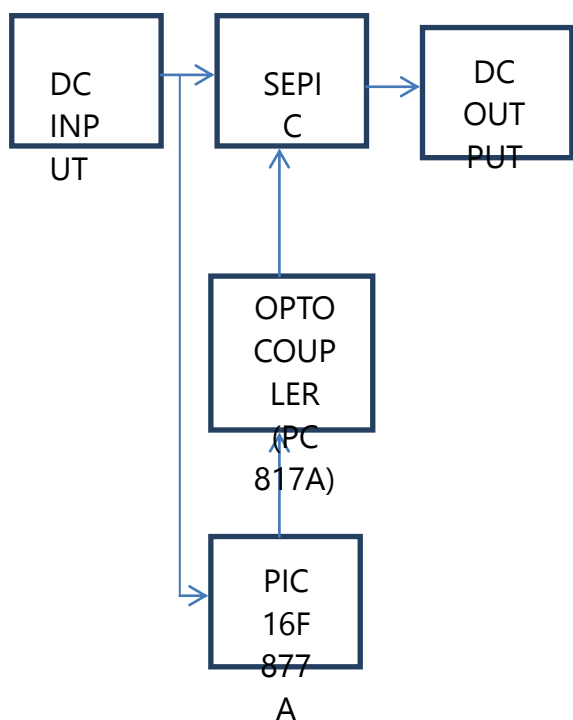


Fig.12. Hardware block diagram

Here the main components used are

- Power supply unit
- PIC Microcontroller (PIC 16F877A)
- SEPIC converter
- Opto coupler (PC817A)

The hardware module is given below. The main requirements of the proposed system are power supply, power circuit and control circuit.

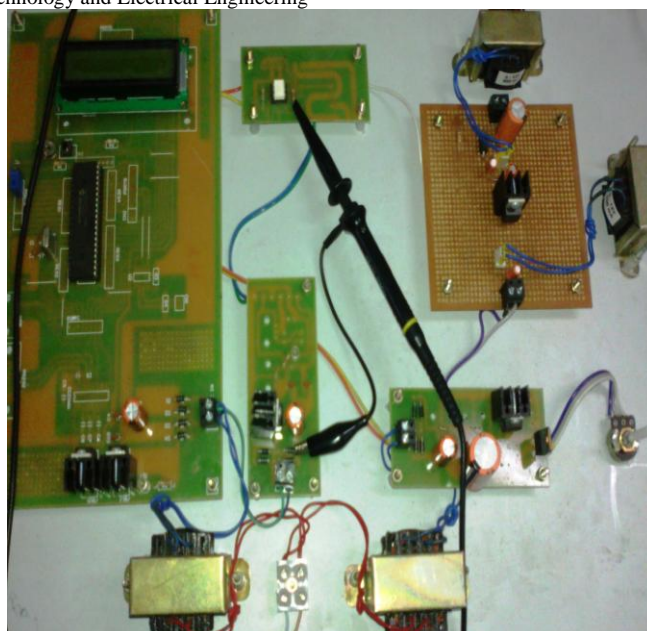


Fig.13. Hardware module

Some of the hardware output results are given below. They are the triggering pulses, input and output waveforms.



Fig.14. Triggering pulses

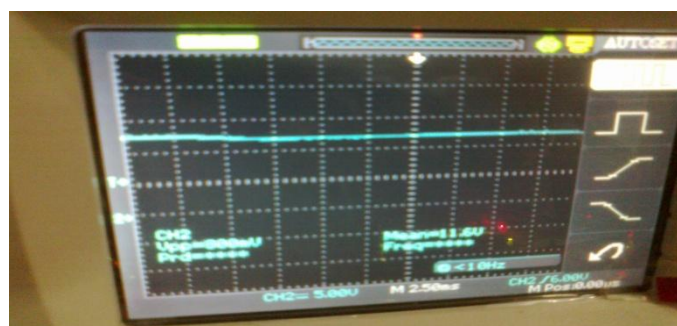


Fig.15. Input voltage



Fig.16. Output voltage

6. CONCLUSION

This work presents a modified resonant SEPIC converter suitable for extremely high-frequency operation and for operating across a wide input and output voltage range. Here we set up a modified SEPIC converter with switching frequency 20 MHz. Its input voltage ranges from 3.6 V to 7.2 V and its output voltage ranges from 3 V to 9 V. Its rated power is 3 W. This work uses an ON-OFF control with fixed frequency. The achievable voltage slew rate is entirely appropriate for applications such as adaptive bias power supplies. It is possible for resonant SEPIC converters to achieve a wide operating range, a small size, and excellent transient response while maintaining good efficiency. It provides fast transient response and good control over wide input and output ranges. In this we can eliminate the bulk magnitude components and facilitates high efficient resonant gating. Soft switching can be achieved for wide input and output voltage ranges. Unlike conventional quasi-resonant and multi-resonant converters no bulk inductor is used and the converter operates at fixed frequency and duty ratio. These attributes reduce passive components size, improve response speed, and enable the use of low-loss sinusoidal resonant gating.

REFERENCES

[1] Jingying Hu, Anthony D. Sagneri, Juan M. Rivas, Yehui Han, Seth M. Davis, and David J. Perreault "High-Frequency Resonant SEPIC Converter with Wide Input and Output Voltage Ranges" *IEEE Transactions on Power Electronics*, vol. 27, no. 1, Jan 2012.

[2] Sweta Srivastav, Sanjay Kumar Singh, "An introduction to Resonant SEPIC converters with variable Input and Output conditions" *International Referred Research Journal*, September, 2011, VOL-II

[3] Sweta Srivastav, Sanjay Singh "An Introduction to Sepic Converters" *International Referred Research Journal*, July, 2011, VOL-II

[4] James R. Warren, III, Kathryn Anne Rosowski, and David J. Perreault, Senior Member, IEEE "Transistor Selection and Design of a VHFDC-DC Power Converter" *IEEE Transactions on Power Electronics*, VOL. 23, NO. 1, Jan 2008

[5] R. C. Pilawa-Podgurski, A. D. Sagneri, J. M. Rivas, D. I. Anderson, and D. J. Perreault, "Very high frequency resonant boost converters," in *Proc. Power Electron. Spec. Conf.*, Jun. 2007, pp. 2718–2724.

[6] J. M. Rivas, "Radio frequency dc-dc power conversion," Ph.D. dissertation, Dept. Elect. Eng. Comput. Sci., Massachusetts Institute of Technology (MIT), Cambridge, Sep. 2006.

[7] Y. Han and D. J. Perreault, "Analysis and design of high efficiency

[8] J. Rivas, R. Wahby, J. Shafran, and D. Perreault, "New architectures for radio-frequency dc/dc power conversion," *IEEE Trans. Power Electron.*, vol. 21, no. 2, pp. 380–393, Mar. 2006.

[9] J. Rivas, D. Jackson, O. Leitermann, A. Sagneri, Y. Han and D. Perreault, "Design considerations for very high frequency dc-dc converters," in *Proc. 37th IEEE Power Electron. Spec. Conf.*, Jun. 18–22, 2006, pp. 1–11.

[10] N. O. Sokal, "Class-ERF Power Amplifiers," *QEX Mag.*, pp. 9–20, Jan. / Feb. 2001.

[11] Juan M. Rivas, Olivia Leitermann, Yehui Han, David J. Perreault "A Very High Frequency dc-dc Converter Based on a Class D Resonant Inverter" *Research Circle eNiskayuna*, Ny 12309

[12] K. Mark Smith Jr. and Keyue M. Smedley "A Comparison of Voltage-Mode Soft-Switching Methods for PWM Converters" *Dept. of Electrical and Computer Engineering University of California, Irvine.*

[13] R. Erickson and D. Maksimović, *Fundamentals of Power Electronics*, Norwell, MA: Kluwer, 2000.

[14] M. Iwaware, S. Mori, and K. Ikeda, "Even harmonic resonant class E tuned power amplifier without RF choke," *Electron. Commun. Jpn.*, vol. 79, no. 1, pp. 23–30, Jan. 1996.

[15] H. Koizumi, M. Iwaware, and S. Mori, "Class E2 dc/dc converter with second harmonic resonant class E inverter and class E rectifier," in *Proc. 3rd Annu. Appl. Power Electron. Conf.*, 1994, pp. 1012–1018.

AUTHOR PROFILES

Ninsha Miriyam Raju received the B.Tech. Degree in electrical and electronics engineering from Carmel Engineering College, Ranni, affiliated to Mahatma Gandhi University, Kerala, India in 2011. Now doing M.E in Power Electronics and Drives in PSNA College of Engineering and Technology, Dindigul, affiliated to Anna University, Chennai. Her interested areas are speed control of ac and dc, ac and dc drives, Digital electronics.

J. Velmurugan received B.E from Coimbatore Institute of Technology [1997] and M. T.E.C.H from Vellore Institute of Technology [2006]. Now working as Associate Professor at PSNA College of Engineering and Technology, Dindigul. His interested areas are Power System planning, Modelling of Electrical Machines, Fuzzy Logic control, Power system Control.

